

## Differences between Apollo V1ix and Apollo V2ix

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<b>PCB Version:</b>	2	<b>Schematic Version:</b>	2
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The Apollo has recently been updated from V1i4 to V2i1 to comply with the European RoHS directive and to overcome component obsolescence.

This document outlines the key changes that might affect user software.

Component	Key Differences
<b>SuperIO</b> was <i>SMSC LPC47M292</i> now <i>SMSC SCH3112</i>	<p><b>GPIO</b> – Some GPIO changes have been made to accommodate pin functionality changes, additional features have been added.</p> <p><b>Fan Control and Hardware Monitor</b> – The internal implementation has changed and some voltages [1.05V, 1.35V] are no longer measured though both are still monitored by the system power sequencing circuitry.</p> <p><b>LAN Disable</b> - Two SuperIO signals have been setup to allow the onboard Ethernet controllers to be disabled by a BIOS setup option.</p> <p>Eurotech Ltd has updated its Linux &amp; XP drivers such that they will work seamlessly with either version of the Apollo.</p>
<b>LEDs</b>	<p>Two LEDs have been added to the APOLLO V2 board:</p> <ol style="list-style-type: none"> <li>1.) Standby Power LED, to show that standby power is present on the board to prevent PCI cards being plugged into a live board; standby power is provided to the PCI slot to support PME wakeup.</li> <li>2.) BIOS Boot LED, this LED will blink after PCI reset and continue to blink until the BIOS overrides the blink behaviour once POST has completed, this allows for BIOS errors to be more easily identified by a customer</li> </ol>
<b>Ethernet</b>	<p>The APOLLO V2 has been modified to support only the Intel 82541PI Gigabit Ethernet controller, this was previously available only on the APOLLO Gigabit board variant.</p> <p>The APOLLO will now provide two network interfaces, a single 10/100 Ethernet interface and a single 10/100/1000 Gigabit Ethernet interface.</p>
<b>User Jumpers</b>	<p>On the APOLLO V1ix, the BIOS used the APOLLO User Jumper 1 for resetting the default BIOS setup options in the CMOS backup EEPROM. To provide full functionality a separate CMOS restore jumper</p>

	location has been provided which frees the USR1 jumper location for general use.
<b>TPM</b>	The Atmel AT97SC3203 (TPM 1.2) has replaced the Atmel AT97SC3201 (TPM 1.1). The AT97SC3203 is supported by the latest revision of the Phoenix TrustedCore BIOS.

If you have any questions about this or about any of our products please contact Eurotech Ltd Technical Support.